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# **SI3420A**

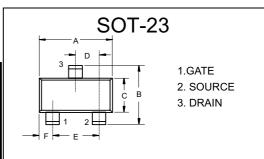
# **Features**

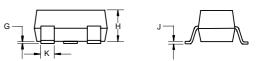
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Halogen free available upon request by adding suffix "-HF"

#### Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
$V_{DS}$	Drain-source Voltage	20	V	
$I_D$	Drain Current-Continuous	6	Α	
I <sub>DM</sub>	Drain Current-Pulsed (Note 1)	30	Α	
$V_{GS}$	Gate-source Voltage	±10	V	
P <sub>D</sub>	Total Power Dissipation	1.25	W	
R⊕JA	Thermal Resistance Junction to Ambient (Note2)	100	°C/W	
$T_J$	Operating Junction Temperature	-55 to +150	$^{\circ}$ C	
T <sub>STG</sub>	Storage Temperature	-55 to +150	$^{\circ}$	

# **N-Channel Enhancement Mode Field Effect Transistor**



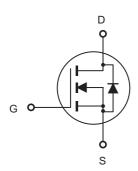


DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Н	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	015	020	37	51	

# Suggested Solder

# Pad Layout inches

# **Internal Block Diagram**





# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

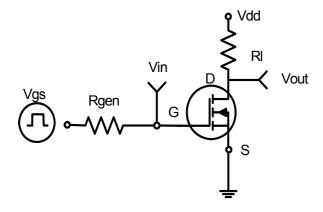
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	20	22	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)		•	•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	0.5	0.7	1.0	V
Davis Course On Otata Basistana	_	V <sub>GS</sub> =2.5V, I <sub>D</sub> =4.0 A	-	27	35	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5.0A	-	20	28	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =6A	-	25	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ 40\/\/ 0\/	-	515	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V, F=1.0MHz	-	90	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVITZ	-	72	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	3	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =10V, $R_L$ =1.7 $\Omega$	-	7.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =10 $V$ , $R_{GEN}$ =3 $\Omega$	-	20	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	6	-	nS
Total Gate Charge	Qg		-	12		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =10V,I <sub>D</sub> =6A,V <sub>GS</sub> =10V	-	1	-	nC
Gate-Drain Charge	$Q_{gd}$		-	2	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =1A		-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	6	Α

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production



## **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 

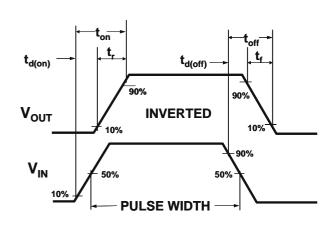
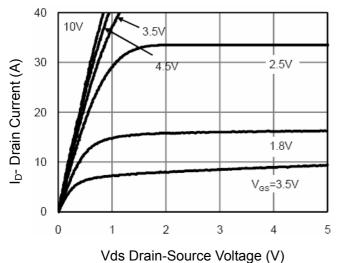
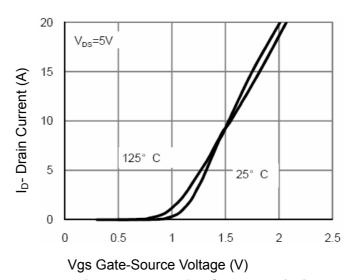


Figure 2:Switching Waveforms



**Figure 3 Output Characteristics** 



**Figure 4 Transfer Characteristics** 

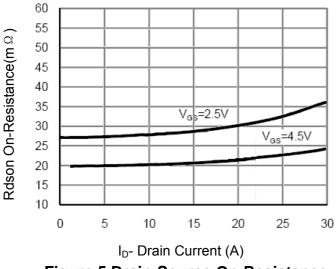


Figure 5 Drain-Source On-Resistance

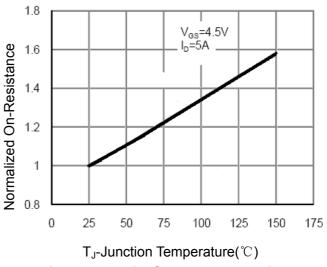


Figure 6 Drain-Source On-Resistance



### **Ordering Information:**

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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